

## Question Bank for PT2

2025-2026, Fundamentals of Microprocessor

Refer PPTs and word file uploaded on teams

### Chapter-4

Sr.No.	Questions	Marks
Q1	Compare microcontroller and microprocessor	4
Q2	Draw and describe the architecture of AVR	4/6/8
Q3	List the features of AVR	4
Q4	Draw the Family structure of AVR	4
Q5	Discuss the ROM , RAM , SRAM and EEPROM of AVR	4/6/8
Q6	List the Application of Microcontroller	4
Q7	Draw and explain the ROM architecture of Atmega32	4/6
Q8	Draw and explain the Harvard architecture of AVR	4/6/8

### Chapter 5

Sr.No.	Questions	Marks
1	Explain the I/O programming, lists its ports and registers	4/6
2	Explain the registers for the PORTB and draw its relation with port pins	6/8
3	Explain with diagram the role of DDRxn in inputting and outputting the data	4/6
4	Write C program to send data 25H to port A(similar questions)	4/6
5	Write the value of the registers for I/O programming for the following the statements ( data may vary) 1) To output data in variable x on port A 2) To output 0xFF data on port B 3) To read the data from port A 4) To output data on only 0th bit of port C	4
6	Define timer and Interrupts, list the timer and interrupts in AVR	4
7	Define timers and explain types of timers	4/6
8	Explain with neat diagram the timer0/1/2 and its registers.	6/8
9	List AVR interrupts and write memory location of it.	4
10	Write sources of AVR interrupts	4

### Chapter6

Sr.No.	Questions	Marks
1	List any Six/eight features of Pentium processor	4/6
2	Draw and explain Pentium superscalar architecture	4/6
3	State need of branch prediction logic of Pentium processor	4
4	Describe the history bits and its role in branch prediction	4/6
5	Explain Branch prediction logic of Pentium processor	6/8
6	Draw and explain all phases of Pentium processor pipelining	4/6

